

4. Schaltplan

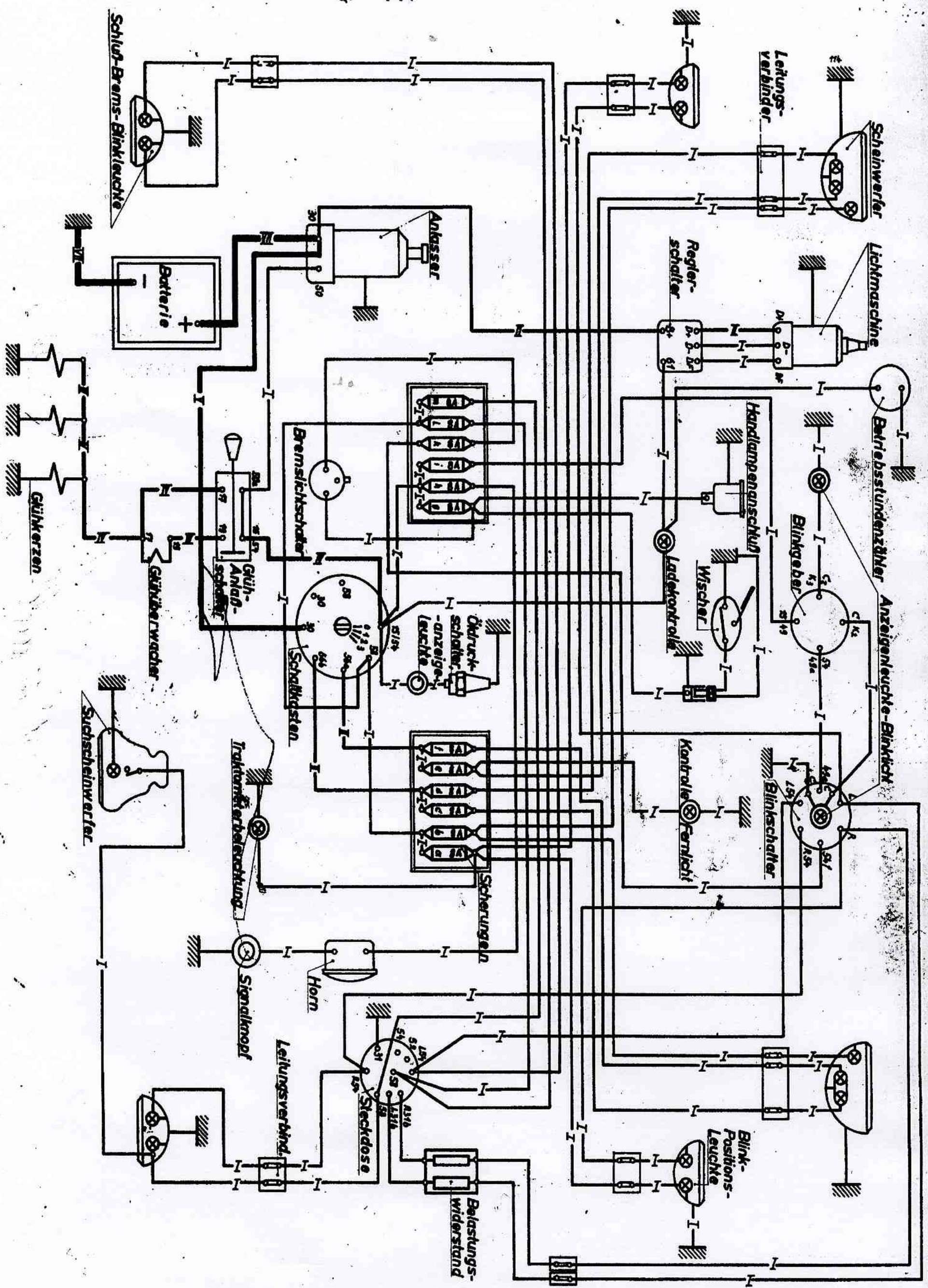


Abb. 43